# Morphological evolution of a single crystal silicon battery electrode during lithiation and delithiation: An *operando* phase-contrast imaging study

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**ABSTRACT** *Operando* phase-contrast radiography combined with impedance spectroscopy and electron microscopy is applied to study the morphological changes in a lithium-silicon cell over several cycles. The single-crystal silicon (100) surface is employed as a working electrode. A checkerboard-like cracking pattern aligned with the crystallographic axis is formed in the 4th cycle during the second step of the delithiation. With the crack position kept fixed, this crack pattern vanishes during lithiation and gets more pronounced in the subsequent cycles. This pattern forms just after the first delithiation peak that corresponds to the decomposition of the highly lithiated phase. It vanishes during lithiation in the potentiostatic step. Therefore, capacity-limited cycling, which partly maintains the highly lithiated phase, may avoid the formation of fractures. The surface area of the crack cells remains almost constant with increasing cycle numbers, and no electrochemically inactive sites were observed.

# **INTRODUCTION**

Lithium-ion batteries are commonly used in daily life and represent the state-of-the-art battery system<sup>1,2</sup>. For this battery type, graphite is the mainly used anode with a theoretical capacity of 372 mAh g-1, which limits the overall capacity <sup>3</sup>. In contrast, silicon has a theoretical specific capacity of 4200 mAh g-1 and, therefore, can replace the graphite anode to increase the overall capacity<sup>4</sup>. This high capacity, combined with the nontoxicity and abundance of silicon, has led to intense research in this field <sup>2–4</sup>. However, the major drawback of this anode material is the volume change of more than 360% during cycling. <sup>5</sup>. This expansion creates internal stress in the silicon and introduces cracks within the electrode, which leads to a noticeable capacity fading <sup>6</sup>.

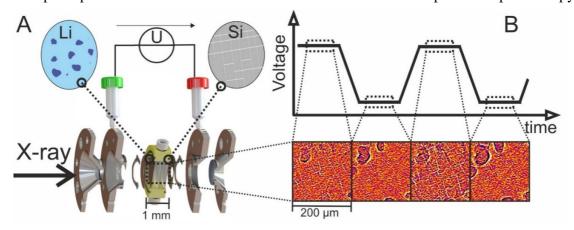
Many studies were conducted to gain deeper insights into the fading mechanisms of silicon electrodes. Diffraction experiments showed the conversion of crystalline silicon into an amorphous phase with different lithium concentrations, but the highly lithiated phase,  $Li_{15}Si_4$ , recrystallizes again <sup>7</sup>. Electron microscopy was used to visualize the lithiation process on the nanoscale and revealed a two-step lithiation mechanism<sup>8</sup>. Different morphologies were studied with electron microscopy, such as nanowires<sup>9</sup>, nanoparticles<sup>10,11</sup>, or porous nanostructures<sup>12,13</sup>. The lithiation is accompanied by a volume expansion, which was shown by theoretical calculations<sup>14–16</sup> and quantified with neutron reflectometry by Jerliu *et al.*  $^{17}$ . The volume expansion is accompanied by tensile or compressive stress, depending on the lithiation degree. For nano-sized particles, this was shown through diffraction measurements and Raman spectroscopy <sup>18,19</sup>. Theoretical calculations linked the lithiation-induced stress with the fracture of different geometries and proposed an upper limit of 150 nm for spheres to avoid fracturization <sup>20</sup>. Indeed, this theoretical limit was confirmed by McDowell et al. and Liu et al., who observed the fracturization process for nano-sized particles using electron microscopy<sup>10,11</sup>. Similar observations were made on the macroscale using radiography or tomography, visualizing the volume expansion and fracturization of micron-sized particles <sup>21–27</sup>. The volume expansion during lithiation damages the host matrix, which leads to a loss of electrical connection and delamination<sup>22,25,27</sup>. Inhomogeneous lithiation was observed using complementary X-ray and neutron imaging <sup>26</sup> as well as the creation of porosity within the electrode <sup>27</sup>. The studies mentioned above

demonstrate a strong dependence of the lithiation process on the electrode morphology <sup>2,22–24</sup>. The interplay between binder, conductive agent, and size distribution of the particles increases the complexity of the system and complicates a mechanistic understanding of the lithiation process. Up to now, no consensus about the aging mechanisms has been achieved yet.

In contrast to the enormous work on particle-based anodes, much less research was done using single crystalline silicon electrodes, although it is a well-defined model electrode <sup>28–34</sup>. Single-crystal silicon allows us to study the lithiation process in a system without any binder or conductive agent. *Operando* neutron and X-ray reflectometry reveal the depth-dependent lithium concentration over several cycles, but no fractures were observed due to experimental limitations <sup>32–34</sup>. The mechanical stress within the (100) silicon was determined by Chon *et al.* depending on the cell voltage <sup>29</sup>, whereas Jana *et al.* elucidated the cycle-dependent change of the residual stress within the (100) Si-crystal <sup>28</sup>. An *ex-situ* electron microscopy study revealed cracking of (100) silicon after several cycles <sup>30</sup>, whereas the crack propagation depends on the crystal orientation <sup>31,35</sup>. However, the dependency of the fractures on the lithiation degree and their evolution is hardly understood. More *operando* studies aiming at the macroscale could improve the mechanistic understanding of the cracking process.

A suitable technique to analyze cracks and material boundaries within a specimen is phase-contrast imaging <sup>36–38</sup>. This technique additionally employs the phase shift of the incident x-ray beam to visualize a specimen, whereas the traditional attenuation-based imaging uses the intensity decrease solely. Therefore, refraction occurs at the boundaries of different refractive indices. This effect leads to a slight deviation of the X-ray paths (propagation-based phase-contrast X-ray imaging) <sup>36,38</sup>. The resulting dippeak pair in the X-ray intensity causes a sharp contrast of interfaces within the sample <sup>37</sup>. For this reason, a monochromatic, parallel, and coherent beam is required. For elements with a low atomic number, the ratio of phase shift coefficient to the absorption coefficient is approximately three orders of magnitude. The phase shift and absorption coefficient correspond to the real and imaginary part of the refractive index, respectively. Therefore, imaging under the X-ray phase-contrast mode is highly suitable to analyze specimens with light elements such as silicon or lithium <sup>36</sup>.

Here we present, to the best of our knowledge, the first *operando* X-ray phase-contrast imaging study of the electrochemical lithiation process of (100) crystalline silicon (Fig. 1). A voltage-controlled cell operation, consisting of a linear sweep, followed by a potentiostatic step, was chosen to promote the formation of different lithiated silicon species and their influence on the electrode morphology. For instance, the formation of the highly-lithiated phase occurs only at voltages < 70 mV.<sup>32</sup> Phase-contrast imaging is susceptible to material boundaries and, therefore, well suited to study the fracturization process <sup>36</sup>. In total, eight cycles were studied. Roughly 150 images were recorded per cycle, which allows us to study the fracturization process under *operando*-conditions. Besides, focussed ion beam microscopy (FIB/SEM) is used to assign the observed characteristics to the specific electrode, since radiography creates a superimposed view of both electrodes. Additional *in-situ* impedance spectroscopy gives



**Fig 1:** A) Schematic sketch of the *operando* setup. A lithium metal chip and a silicon wafer are used as electrodes. A potentiostat controls the cell voltage. The silicon cracks during delithiation. B) Schematic sketch of the applied voltage profile. At high voltages (delithiation), cracks become visible in the obtained radiograms. After lowering the voltage, 2 lithiation starts, and the cracks disappear. The sequence was repeated over eight cycles. X-rays generated from a synchrotron source (BAMline station at BESSY II) are used to elucidate the internal processes. The resulting image is based on the refraction of X-rays on material interfaces (phase-contrast imaging)

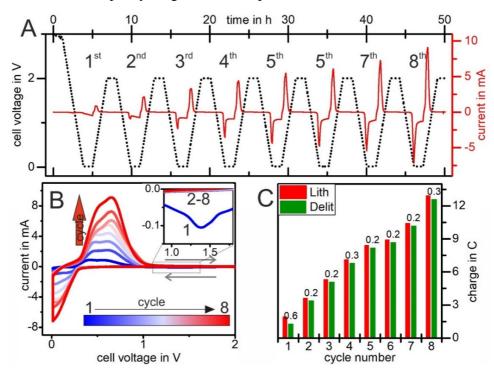
information about the electrochemical properties of the system, and ex-situ Laue diffraction helps to correlate the observed morphology changes to the crystallographic axis. Therefore, we provide mechanistic insights into the voltage-dependent fracturization of silicon electrodes. First, the essential electrochemical performance of the experiment will be compared to systems described elsewhere<sup>4,30,39</sup>. Afterward, the results obtained by *operando* radiography will be discussed, followed by the observations from *ex-situ* studies, namely FIB/SEM and Laue diffraction.

#### **RESULTS AND DISCUSSION**

#### Analysis of the electrochemical performance

Figure 2 summarizes the acquired electrochemical results. The voltage excitation is indicated as the black dotted line shown in Fig 2A, whereas the solid red line shows the current response of the system. The cycle-dependent increase in current is caused by the successive increase in electrochemically active silicon.<sup>30</sup>. This is a consequence of the amorphization of silicon of prior cycles. As shown in figure 2B, the lithiation of the amorphous phase occurs already at 300 mV<sup>7,34,40</sup>, while the lithiation of the crystalline phase in the first cycles starts at voltages lower than 100 mV <sup>4,7,30</sup>. Only the first cycle shows a peak at 1.4 V during voltage ramp down (inset in figure 2B). Therefore, this can be associated with the lithiation of the silicon oxide layer superimposed with the removal of adsorbed species. At 0.4 V and 0.6 V, two delithiation peaks can be found, which shifts slightly to higher voltages (560 mV in the first cycle, 650 mV in the 8<sup>th</sup> cycle for the second delithiation peak). Both peaks are comparable in height; afterwards, the high voltage peak becomes significantly larger. Shi *et al.* <sup>30</sup> also observed this effect in a similar experiment with a lower voltage rate of 0.1 mV/s. This result indicates a diffusion limitation of the dissolution of the lowly lithiated silicon phase, as suggested by Pharr *et al*<sup>35</sup>. No off-set current, and no additional current peaks can be observed.

The charge balance in fig. 2C shows a constant irreversible charge loss of about 0.2 C from the second cycle on. This discrepancy originates from parasitic side reactions associated with the solid-



**Fig. 2:** A) Time-dependent cell voltage set by the potentiostat (dotted black line) and the corresponding current response (solid red line). B) Cyclic voltammetry plots for cycle 1 (red) to 8 (blue). The inset shows the region around the high voltage peak, which only occurs in the first cycle. The grey arrows indicate the direction of the voltage change. C) Comparison of lithiation (red) and delithiation (green) charge. The charge difference is nearly constant, except for the 3 first cycle.

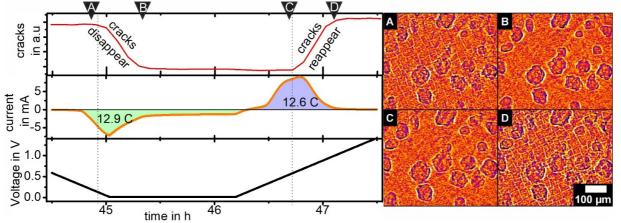
electrolyte-interphase layer (SEI). The first cycle shows a higher charge loss of 0.6 C caused by a small peak around 1.4 V (inset Fig 2B). With cycling, the coulombic efficiency increases from 0.68 to 0.98 since the incorporated charge increases, but the irreversible charge loss stays constant. Most probably, the charge loss originates from parasitic side reactions such as the reformation of a SEI<sup>33,43,44</sup>. The acquired electrochemical data exhibit all significant features of the lithiation of silicon <sup>4</sup>. Therefore, the custom-designed *operando*-cell allows us to link the imaging results to the corresponding electrochemical processes.

#### **Operando-results**

*Operando* phase-contrast radiography was performed to link the electrochemical processes and structural changes within the cell. The X-ray beam penetrates several layers, including the metal lithium electrode, separator, and silicon electrode. Due to the high stability of the separator (Celgard 2700), no morphological changes of the separator within the eight cycles were observed. Therefore, the observed structural changes in the experiment are attributed only to both electrodes. The assignment of the observed morphology changes to the respective electrode was done by investigating the cycled silicon electrode and are created through the lithium pitting process <sup>45</sup>. The straight lines can be found on the silicon electrode and originate from the crack pattern of the silicon caused by the volume expansion and the brittle nature of the electrode. The radiography images were processed to get a qualitative view of the morphology changes (see also experimental section). The full video file is available online. First, a single cycle will be discussed more in detail to correlate the occurrence of cracks within the silicon with the cell voltage. The 8<sup>th</sup> cycle is chosen as an example since the cracks are most pronounced here (figure 3). Afterwards, evolution over many cycles will be discussed (figure 4).

The crack intensity of cycle eight is shown in the top row, the current in the middle, and the voltage is plotted in the bottom row of figure 3. Four radiograms are shown in the right part to demonstrate the status of the electrodes at specific voltages. The first image (A) shows the electrode during the lithiation step before the cracks start to vanish (around 120 mV). Once the voltage is lowered to 15 mV, the silicon electrode gets smooth, and the cracks are not visible anymore. Only the pits, formed during the lithium anode corrosion, remain in the field of view <sup>45</sup>. The voltage indicates the lithiation of crystalline silicon and the formation of the highly lithiated phase  $Li_{22}Si_5$ <sup>4</sup>. Therefore, the volume expansion of the silicon fills the interspaces. In literature, an anisotropic expansion of silicon is reported<sup>46,47</sup>, but not observed here since all cracks seem to vanish simultaneously. A possible reason is the limited in-plane-space within the silicon matrix, which hinders a deformation of the silicon patches. A total charge of 12.9 C (7.9 C cm-2) was deposited within the silicon electrode, the majority during crack closure.

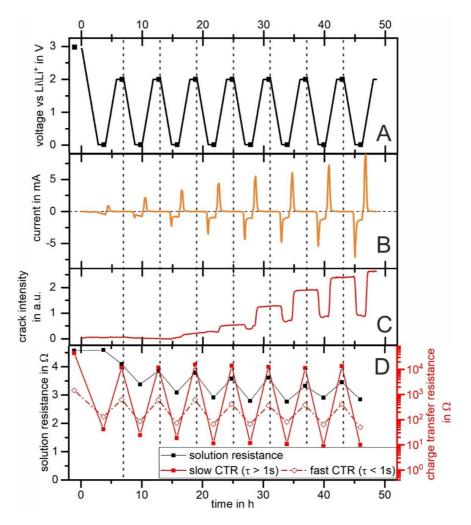
The inverse process is shown in the images C and D, where the cracks reappear. Image C shows the intermediate stage between the two delithiation current peaks. It is noteworthy to mention that



**Fig. 3**: Correlation of electrochemical data and structural change in the  $8^{th}$  cycle. The crack intensity is shown on the top (left side). The current is plotted in the middle row, and the voltage profile is in the bottom row. Note that not the full cycle is shown but only the part where morphological changes occur. The respective radiograms assigned by the letters A-D are shown on the right.

approximately half of the lithium was removed already from the silicon before the cracks became rapidly visible. This observation demonstrates that silicon can withstand a certain stress level before macroscopic effects become visible, which is the cracking of the brittle silicon. The current peak around 500 mV is associated with the delithiation of the highly lithiated phase, whereas the peak at 650 mV is ascribed to the complete lithium removal <sup>30,41,48</sup>. So, limiting the lithiation degree to a specific range might avoid the fracture of the silicon electrode surface, which was investigated already by Obrovac *et al.* in an *ex-situ* study<sup>40</sup>. In their work, they suggested a charge limited lithiation process to preserve high charge retention. The voltage-dependent morphology change of the first and seventh cycle, as well as the charged and discharged state of all cycles, is shown in the supporting information (Fig. S1)

Figure 4 summarizes the electrochemical data and the obtained crack intensities over the whole experiment. Cell voltage and current are shown in Fig. 4A and B, whereas 4C shows the crack intensity. Fig. 4D illustrates the results of the impedance spectroscopy at the end of each potentiostatic step. As already discussed, the current increases with the cycle number due to the ongoing amorphization of silicon <sup>4,7</sup>. A slight increase in the crack intensity can be found in the first two cycles, but no fracturization pattern can be observed. Instead, the wetting process can be observed as well as a roughening of the surface. Both processes may lead to this artifact in the crack intensity plot within the first two cycles. The first cracks appear in the third delithiation and get more pronounced from cycle to cycle. This observation yields a possible explanation for the increase in the high voltage peak during the delithiation from the fourth cycle



**Fig. 4**: A) Voltage profile of the cell during the experiment. The black squares denote the measurement of an EIS B) Corresponding current of the cell. C) Crack intensity obtained from processed radiograms. The crack intensity increases in the delithiated state and decreases in the lithiated state. The intensity originates from the phase-contrast evolving at phase boundaries. D) Solution resistance and charge transfer resistances for fast ( $\tau < 1$  s) and slow ( $\tau > 1$ s) charge transfer processes. Clearly, alternating behavior can be found. The used equivalent circuit is shown in fig. S2, together with the 5 obtained data. The time axis is shared by all four graphs. Each dashed line indicates the end of a cycle.

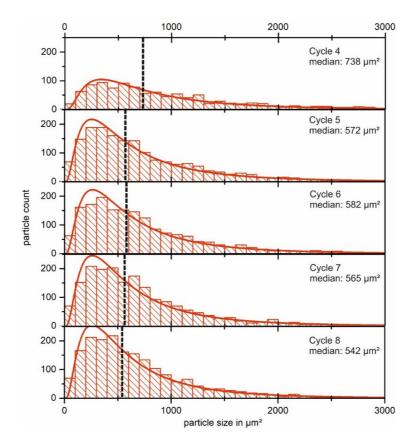
on. The cracking of the electrode increases the electrochemically active area. Therefore, the diffusion limitation of the lithium ions in the amorphous silicon phase, as suggested by Pharr *et al.*<sup>35</sup>, is circumvented by now available shorter diffusion paths to the silicon/electrolyte interphase.

The crack intensity decreases during the lithiation process due to the volume expansion of the silicon electrode, especially during the potentiostatic step. Both electrochemically induced morphology changes take approximately 40 minutes. The crack intensity increases again during delithiation between the two cathodic peaks at approximately 600 mV. Here the volume shrinks, and fractures within the brittle silicon are revealed. The crack intensity increases with cycle number since the area of the silicon surface covered with fractures increases, and the contrast of the individual cracks increases with cycling. This represents the mechanical aging of the electrode. However, no inactive spots on the silicon electrode were observed during the *operando* imaging experiment, which would be indicated by certain places that do not undergo morphological changes anymore. The irreversible charge loss is shown in fig. 2C is therefore caused by parasitic side reactions associated with the SEI<sup>3,4,3,44</sup>.

A lab-based galvanostatic cycling experiment in a standard coin cell (CR2032) shows that even after 40 cycles, the charge retention of this system stays close to unity, and no electrode failure was observed (Fig. S3 in the supporting information). The spallation of active material from the electrode would decrease the coulombic efficiency since the fractures form after half of the charge is extracted. However, the spallation of silicon after complete delithiation cannot be excluded. The results of the impedance spectroscopy reveal an alternating behavior. The charge transfer processes are subdivided into a fast regime ( $\tau < 1$  s) and a slow regime ( $\tau > 1$  s). Both charge transfer resistances (CTR) are higher in the delithiated state than in the lithiated state. The amplitude is higher for the slow relaxation processes. The fast relaxation processes around 1 kHz are attributed to the lithium electrode <sup>49–51</sup>. The slow relaxation processes take place mainly at the silicon electrode and correspond to the formation and dissolution of an inhibition layer <sup>34</sup>. Also, the solution resistance shows an alternating behavior. The solution resistance is decreased and increased in the lithiated and delithiated state, respectively. This can be explained by the growth of a layer on the electrodes, which introduces additional resistance, as well as, the charge-state dependent conductivity of lithiated silicon<sup>52</sup>. Also, the different mobility of lithium within lithiated and non-lithiated silicon influences the resistance<sup>53</sup>. An electrolyte decomposition process would be accompanied by a constant increase in the solution resistance. Nyquist plots of all states are shown in the supporting information (Fig. S2).

The size of the silicon crack pattern cells was determined in the final states of the 4<sup>th</sup> to 8<sup>th</sup> delithiation (Fig. 5). The total number of identified cells increases from approx. 1050 after the fourth cycle to 1878 within the 8<sup>th</sup> delithiation. The analyzed area was 0.9 x 1.3 mm. However, the median area decreases from roughly 738  $\mu$ m<sup>2</sup> to 572  $\mu$ m<sup>2</sup> from the 4<sup>th</sup> to the 5<sup>th</sup> cycle, respectively. Afterwards, the median size stays nearly constant. Similar behavior is found if the first quartile is used for the data evaluation (309  $\mu$ m<sup>2</sup> and 310  $\mu$ m<sup>2</sup> after the 5<sup>th</sup> cycle, respectively). Therefore no significant continuous fracturization of the already formed cells occurs. The final state of the 8<sup>th</sup> delithiation was analyzed further to get information about the length and width of the cells (Fig. S4)

4 in SI). The smallest box still encasing the individual cell was chosen as a size parameter. The shorter edge was taken as width, the longer one as the length of the box. The mean box length is 23.9  $\mu$ m, whereas the mean box width is 16.3  $\mu$ m, indicating a rectangular shape of the cells. Less than 5% of all cells are smaller than 6  $\mu$ m in width or 9  $\mu$ m in length. Due to the special morphology, the silicon electrode may behave more like silicon particles/rods than like a silicon layer and also lithiate anisotropically<sup>35,47,54</sup>. Therefore, even structures in the  $\mu$ m-scale can withstand pulverization caused by mechanical fracturization. This behavior may originate from a limited lithiation degree<sup>20,40</sup>. Also, an anisotropic lithiation of the silicon patches may suppress the fracturization. However, also smaller, disordered cracks were reported elsewhere for different geometries<sup>21,30,55</sup>, which may be outside of the experimental resolution. Therefore, FIB/SEM was used for further analysis of the specimen.

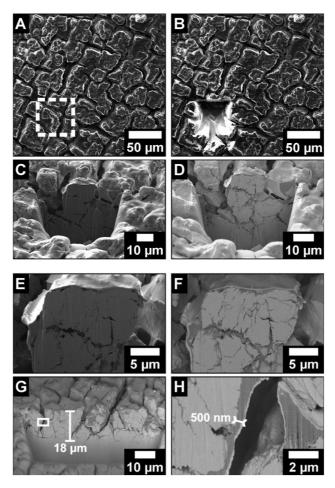


**Fig. 5**: Size distribution of the crack cells on the silicon electrode for the cycles 4-8. The size distribution can be described by a logarithmic normal distribution. The dashed line indicates the median area size. Areas larger than  $3000 \,\mu m^2$  were treated as uncracked and therefore have been removed.

#### Ex-situ morphology analysis

Figure S5 shows an overview image of the final state of the cell after 15 cycles. Afterward, the cell was disassembled in the delithiated state, and the silicon electrode was placed in a focused ion beam/electron microscopy. The results are shown in figure 6. Clearly, a crack pattern can be found which follows the directions of the crystal planes (Fig. 4). This was proven by *ex-situ* Laue diffraction (Figure S6 in SI). As already mentioned, the crystalline silicon gets amorphous during the initial stage of the lithiation process <sup>4</sup>. Therefore, the cracking process starts already beforehand, since otherwise, no preferred direction should exist. Acoustic emission studies support these findings <sup>56</sup>. In the successive delithiation, the fractures become visible on the macroscale. No growth of fractures was observed during the lithiation process. This is in contrast to observations done by Bordes *et al.*, who found fractures on micron-sized silicon particles during the first lithiation<sup>42</sup>.

The depth of the cracks is approximately  $18 \,\mu\text{m}$  (Fig. 6G). Also, cracks within the pillars were found, but they are thinner than the regular crack pattern and are not aligned with the crystallographic axes. Our findings are comparable to the study by Shi *et al.*, who also found similar characteristic patterns in their *ex-situ* study <sup>30</sup>. Based on the depth of  $18 \,\mu\text{m}$  and a charge of 26 C in the  $15^{\text{th}}$  cycle (16 C/cm<sup>2</sup>), an average lithiation degree of 1.1 lithium atoms per silicon can be calculated. This is just a rough estimation, since the silicon most probably lithiates anisotropically and also the cracked depth not necessarily is similar to the thickness of the lithiated region. The estimation is by a factor of 2 smaller than values found in previous studies <sup>32–34,41,43</sup>. This could be explained by an inhomogeneous lithiation of the pattern cells since the electrochemical results indicate the formation of the highly lithiated silicon interphase (Li<sub>15</sub>Si<sub>4</sub> and Li<sub>22</sub>Si<sub>5</sub>)<sup>4</sup>.



**Fig. 6**. Ex-situ FIB/SEM after 15 cycles. A) Overview image of the checkerboard pattern. B) FIB-cut of the marked area in A. C) &D) magnified view of cut-area from b) using two different detectors (SE2-Detector and InLens BSE detector). E) &F) magnified view of one pillar with two different detectors. G) Depth of cracks in the silicon shown in a broader cut-out region. H) Magnified view of the marked area in G) showing the thickness of a coating layer.

# CONCLUSION

The morphological changes of a single crystal (100) silicon electrode during lithiation and delithiation were analyzed over eight cycles using *operando* phase-contrast radiography. The main results can be summarized as follows:

- 1. The time-resolved formation of an orthogonal fracturization pattern was observed. The crack pattern is aligned with the crystallographic axis of the crystalline silicon, as proved with Laue diffraction.
- 2. This pattern forms just after the first delithiation peak that corresponds to the decomposition of the highly lithiated phase, demonstrating certain robustness of the silicon anode towards induced mechanical stresses. The pattern vanishes during lithiation in the potentiostatic step. Therefore, capacity-limited cycling, which partly maintains the highly lithiated phase, may avoid the formation of fractures.
- 3. The cracks reappear in the same position again during the subsequent cycle, but also additional cracks can be found. Most likely, this is caused by the volume expansion of the patches during lithiation, which leads to physical contact between the patches, but the crack is still existent. No interspace between the patches exists in the lithiated space since this would introduce a signal in the phase-contrast radiography. This observation is only possible with *operando* experiments

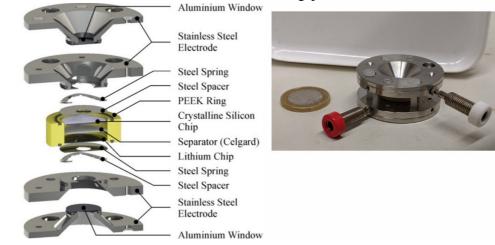
4. However, the area distribution of the fracture pattern cells stays nearly constant, meaning the individual cells can resist further fracturization.

Nevertheless, the continuous growth of the crack pattern may damage the electrode irreversibly at a certain point. Therefore long term *operando* experiments with even higher cycle numbers or a limited capacity operation of single-crystal silicon anodes could reveal further mechanical ageing mechanisms.

## **EXPERIMENTAL**

We employed phase-contrast X-ray imaging to probe morphological changes in a lithium-silicon cell during cycling. The experiment is schematically shown in figure 1 and was performed at the BAMline end-station at BESSY II<sup>58</sup>. The measurement was performed in the phase-contrast regime using parallel, monochromatic, and coherent X-rays. The incoming beam gets deflected at differences in the refractive index (e.g., on material boundaries or cracks) as described by Snell's law of refraction. The refracted beam interferes with the regular X-rays, which highlights the physical boundaries mentioned beforehand. Therefore, the sample-detector distance needs to be adjusted according to the sample properties. An energy of 25 keV was selected with the double multilayer monochromator. The detector system consists of a 60 µm-thick CdWO<sub>4</sub> scintillator in combination with an optical peter lens system (20 times magnification) and a PCO.4000 camera (PCO AG) as shown in figure S7. The distance between the scintillator and the sample was 40 cm during this experiment. This imaging setup yields a 1.75 x 1.17 mm<sup>2</sup> field of view with a pixel size of 0.437 µm, and every frame was exposed for 60 s. The sample environment was shifted out of the beam after every image, and a flat-field image was captured. This procedure results in a time resolution of 2.4 minutes per normalized radiogram. The total experiment time was 48.5 hours. Due to technical difficulties, the imaging process was paused after 20 hours for 1 hour. The electrochemistry was not stopped during that period. The missing 25 radiograms were added later via interpolation. In total, 1220 radiograms were recorded. The acquired images are a superposition of all cell parts, especially of the two electrodes and the separator.

The designed operando cell is shown in Fig. 7  $^{57}$ . The X-ray-windows were made of aluminum, and the cell body was made of PEEK. All other parts were made of stainless steel. A 5-mm hole was drilled into both steel spacer to enhance X-ray transmission. Various ethylene propylene diene monomer rubber rings were used as sealing to avoid air and moisture contamination. Single crystal silicon disks with a polished (100) surface (n-doped, 500 µm thickness, specific resistivity 10<sup>-3</sup> to 40  $\Omega$  cm, Sigma Aldrich) were used as the working electrode after cutting into 16 mm disks using a laser-cutting system. The silicon chip was immersed in 2-propanol (purity >99.8%, Sigma Aldrich, H<sub>2</sub>O<0.1%) for 24 h to remove contaminations from the surface, such as dust or remains from the cutting process. A 1 M LiPF<sub>6</sub> solution in ethylene



**Fig. 7**: Design of the *operando* cell used for the experiment<sup>57</sup>. On the left, a schematic view is shown, including the labeling of the different parts. On the right, a photo of the whole cell is shown together with a one euro coin for size 9 comparison.

carbonate/dimethyl carbonate (1:1 by volume, battery grade, Sigma Aldrich) was used as the electrolyte. Metallic lithium foil serves as the counter electrode. A porous polymer foil (Celgard 2700) is employed as a separator. The cell was assembled in an argon-filled glovebox (H<sub>2</sub>O- and O<sub>2</sub>–level less than 5 ppm). A solution resistance of 4.5  $\Omega$  was measured in the virgin state. The transmittance of the cell was roughly 50%.

All of the image post-processing was done with ImageJ <sup>59</sup>. As the sample is moved for taking flat-fields, and the spatial resolution of the setup is below 1 µm, a drift was inevitable. This spatial shift was corrected using algorithms based on SIFT (Scale-Invariant Feature Transform)<sup>60</sup>. Due to the long exposure time and movement of the monochromator mirrors, a few horizontal stripes where still present in the normalized data. They were partially removed from the radiograms by a wavelet-based stripe filter <sup>61</sup>. The detailed procedure of image correction is described in figure S8. The stack of radiograms was divided by the median of the first ten radiograms to improve the contrast of the phase edges developing in the cell over time. Five radiograms (at times 23 h, 29 h, 35 h, 41 h, and 47 h, respectively) were selected where the cracks were most dominant. A new temporary stack was built with these images. The intensity of the phase shift at the cracks increased, and their position stayed the same. Since other structures were constantly changing in this temporary stack, the median image represents only the crack pattern. With a threshold it was then possible to build a mask representing the position of the cracks and thus calculating the crack refraction intensity over time. A random forest classifier was trained for the final retrieval of size distribution of the crack cells on the silicon<sup>62</sup>. Here the previously build mask for the crack positions was used for manual ground truth generation. The training set consisted of 0.3 % of the whole dataset. A 10-fold cross-validation was applied for training the 200 decision trees. The trained classifier was used to generate probability maps of the spatial crack distribution on the silicon electrode. At last a watershed was applied on the probability map resulting in the cycle dependent decomposition of the crack patterns. For the ex-situ FIB/SEM measurements, the silicon electrode was removed within an Ar-glovebox (H2O, O2 < 1 ppm) from the cell and dried in vacuum at elevated temperatures. No rinsing with solvent was applied to avoid destroying the morphology of the silicon surface. The sample was sealed in a specialized, airtight sample shipper for the transfer into the FIB/SEM sample chamber. Here a short exposure to ambient conditions took place (approx., 15 s). A ZEISS crossbeam 340 was used, which is equipped with a Gemini I electron gun and a Gallium ion source at an angle of 54°. First, the selected volume in the silicon electrode was sputtered with a gallium ion current of 15 nA at 30 keV. For a final surface polishing, an ion current of 300 pA at 30 keV was applied. The milled surfaces have then been scanned using the SE2-Detector and InLens BSE detector with a low electron acceleration voltage of 1 keV. The scanning angle of 36° has been corrected to achieve isotropic spatial dimensions.

For Laue-diffraction, an in-house built Laue camera with a Tungsten X-ray tube operating at 25 kV was used. The data was recorded on a 12x12 cm<sup>2</sup> image plate, which is read out with a pixel size of 100  $\mu$ m. The data were analyzed with the "OrientExpress" software. The brittle silicon wafer cracked before being removed from the cell. Therefore the angle between the crack and the top of the cell was determined. Afterwards, the silicon piece was glued on the sample table of the diffractometer with the breaking edge in the horizontal orientation

Electrochemical measurements were carried out using an interface 1000 potentiostat by GAMRY Instruments. First, impedance spectroscopy (EIS) at open circuit potential was performed (1 MHz to 0.1 Hz, 15 pts/decade, 5 mV root-mean-square (rms) amplitude) at open circuit potential (OCP) followed by a 1 h-OCP-measurement for wetting the electrodes and alignment of the beamline instrument. Afterward, a linear voltage ramp was applied (starting at the last measured voltage, down to 15 mV, 0.3 mV s-1) followed by a potentiostatic step of 1h. At the same potential, an EIS was performed before ramping up the voltage to 2.0 V (0.3 mV s-1). Again, the potential was kept constant for 1 h followed by an EIS. The voltage-time-plot can be found in Fig. 4. Impedance data were analyzed using the equivalent circuit

model, which is shown in the SI. Artifacts in the impedance data caused by the slower scan time at low frequencies were removed using the ZHiT-algorithm before analysis <sup>63</sup>.

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